Electronic Acknowledgement Receipt				
EFS ID:	1139127			
Application Number:	10695336			
Confirmation Number:	4852			
Title of Invention:	Method of controlling grain size in a polysilicon layer and in semiconductor devices having polysilicon structures			
First Named Inventor:	Peter J. Geiss			
Customer Number:	30449			
Filer:	Jack P. Friedman/Tina Holford			
Filer Authorized By:	Jack P. Friedman			
Attorney Docket Number:	BUR920010184US2			
Receipt Date:	03-AUG-2006			
Filing Date:	28-OCT-2003			
Time Stamp:	11:18:51			
Application Type:	Utility			
International Application Number:				
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Payment information:

Submitted with Payment	no
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File Listing:

Docume Numbe	1)ocument Description	File Name	File Size(Bytes)	Multi Part	Pages
1	Amendment - After Non-Final Rejection	BUR920010184US2-Amend 3.pdf	334050	no	13

Warnings:	
Information:	
Total Files Size (in bytes):	334050

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New Applications Under 35 U.S.C. 111

If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

National Stage of an International Application under 35 U.S.C. 371

If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.